

# PIC Optimization and Sensitivity Analysis

October 4, 2018



**3DEXPERIENCE®**

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Validated by: Name

## Overview

eSeminar title	Design Optimization and Sensitivity Analysis of Photonic Integrated Circuits using Physical and Circuit-Level Simulations
Short title	PIC Optimization and Sensitivity Analysis
Live date eg. May 5, 2017	October 4, 2018
Time	10 a.m. PDT (US/Los Angeles) 1 p.m. EDT (US/New York) 7 p.m. CEST (Europe/Berlin) 12 midnight. ICT (Asia/Bangkok)
Duration	60 min
Language	English

## Tracking URLs

Please use the following URLs for promoting the eSeminars!

Event listing	< <a href="https://www.cst.com/events/eseminars/pic-2018?utm_source=hfe&amp;utm_medium=listing&amp;utm_content=pic&amp;utm_campaign=series2018">https://www.cst.com/events/eseminars/pic-2018?utm_source=hfe&amp;utm_medium=listing&amp;utm_content=pic&amp;utm_campaign=series2018</a> >
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## Texts

### Web

eSeminar short description (50 words) + CTA

This eSeminar demonstrates how combining powerful software packages enables engineers designing PICs to automate combined circuit level and full wave simulation. This allows the study of the effect of small fabrication variations in the components on the overall circuit.  
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